

Silicon Diode

BYV99

600V/1A

DATASHEET

OEM – Philips

Source: Philips Databook 1999

Ultra fast low-loss controlled avalanche rectifier

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FEATURES

- Glass passivated
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

DESCRIPTION

Rugged glass SOD57 package, using a high temperature alloyed construction.

This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.

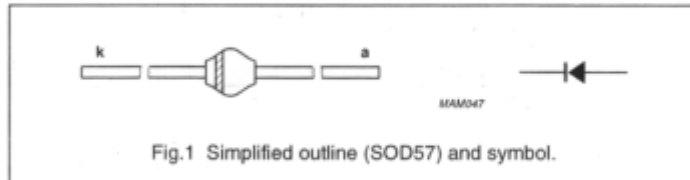


Fig.1 Simplified outline (SOD57) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{RRM}	repetitive peak reverse voltage		–	600	V
V_R	continuous reverse voltage		–	600	V
$I_{F(AV)}$	average forward current	$T_{tp} = 50\text{ °C}$; lead length = 10 mm see Fig. 2; averaged over any 20 ms period; see also Fig. 6	–	1.00	A
		$T_{amb} = 60\text{ °C}$; PCB mounting (see Fig.10); see Fig. 3; averaged over any 20 ms period; see also Fig. 6	–	0.55	A
I_{FRM}	repetitive peak forward current	$T_{tp} = 50\text{ °C}$; see Fig. 4	–	9	A
		$T_{amb} = 60\text{ °C}$; see Fig. 5	–	5	A
I_{FSM}	non-repetitive peak forward current	$t = 10\text{ ms}$ half sine wave; $T_j = T_{jmax}$ prior to surge; $V_R = V_{RRMmax}$	–	20	A
E_{RSM}	non-repetitive peak reverse avalanche energy	$L = 120\text{ mH}$; $T_j = T_{jmax}$ prior to surge; inductive load switched off	–	10	mJ
T_{stg}	storage temperature		–65	+175	°C
T_j	junction temperature		–65	+150	°C

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BYV99**ELECTRICAL CHARACTERISTICS** $T_J = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_F	forward voltage	$I_F = 1\text{ A}$; $T_J = T_{J\text{max}}$; see Fig. 7	–	–	1.5	V
		$I_F = 1\text{ A}$; see Fig. 7	–	–	2.7	V
$V_{(BR)R}$	reverse avalanche breakdown voltage	$I_R = 0.1\text{ mA}$	700	–	–	V
I_R	reverse current	$V_R = V_{RRM\text{max}}$; see Fig. 8	–	–	5	μA
		$V_R = V_{RRM\text{max}}$; $T_J = 150\text{ °C}$; see Fig. 8	–	–	75	μA
t_{rr}	reverse recovery time	when switched from $I_F = 0.5\text{ A}$ to $I_R = 1\text{ A}$; measured at $I_R = 0.25\text{ A}$; see Fig. 12	–	–	15	ns
C_d	diode capacitance	$f = 1\text{ MHz}$; $V_R = 0\text{ V}$; see Fig. 9	–	75	–	pF
$\left \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from $I_F = 1\text{ A}$ to $V_R \geq 30\text{ V}$ and $dI_F/dt = -1\text{ A}/\mu\text{s}$; see Fig.11	–	–	3	$\text{A}/\mu\text{s}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{\text{th j-tp}}$	thermal resistance from junction to tie-point	lead length = 10 mm	46	K/W
$R_{\text{th j-a}}$	thermal resistance from junction to ambient	note 1	100	K/W

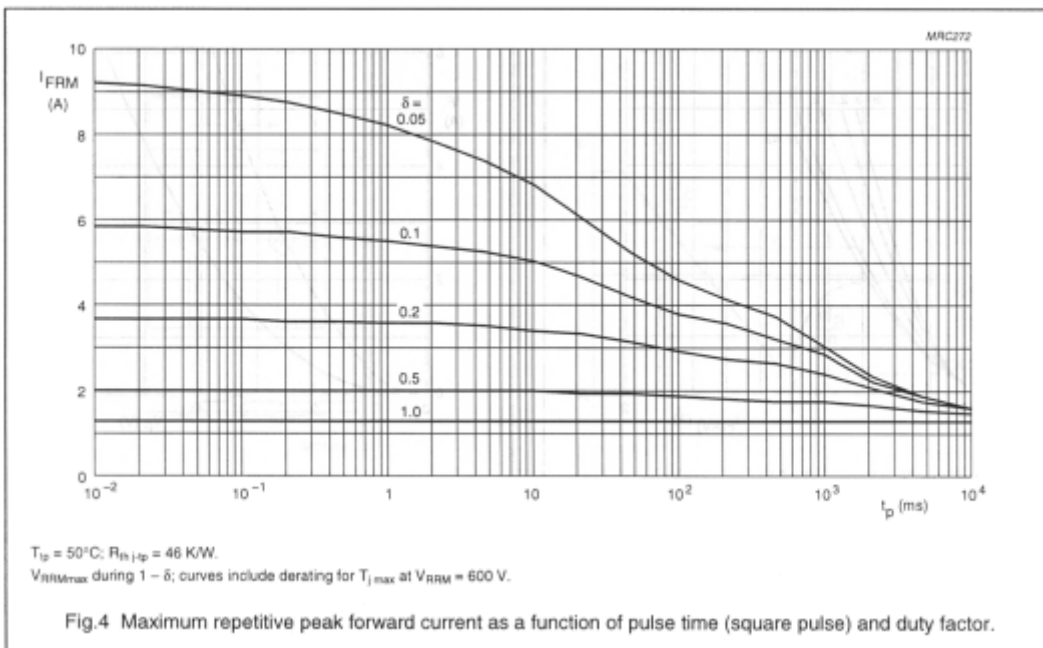
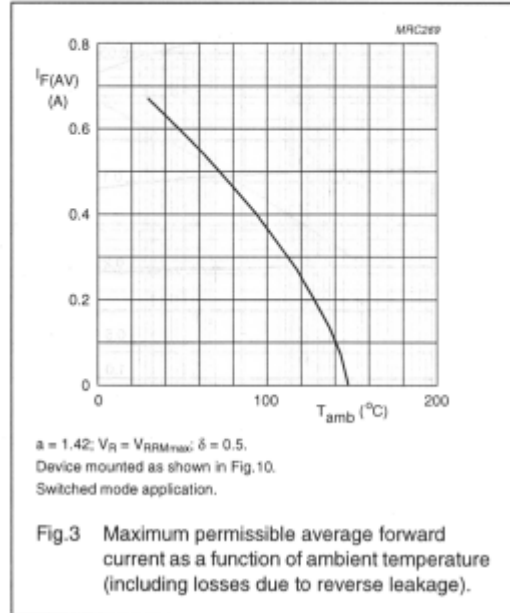
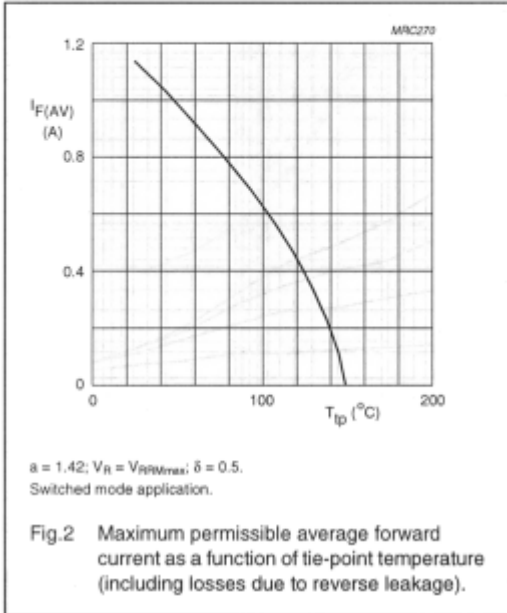
Note

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer $\geq 40\text{ }\mu\text{m}$, see Fig.10. For more information please refer to the 'General Part of Handbook SC01'.

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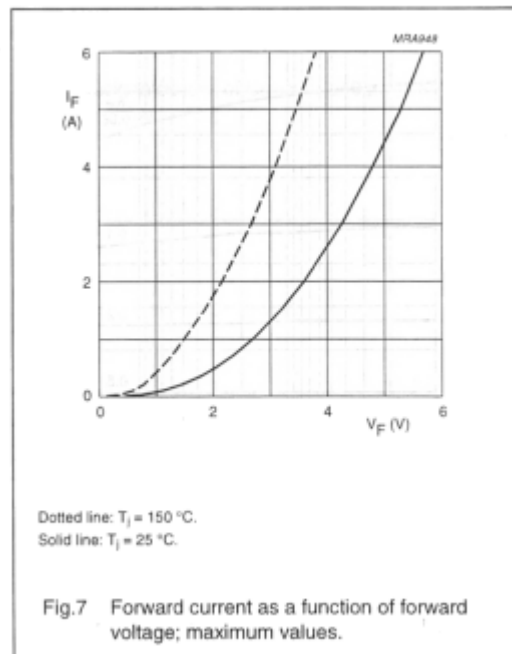
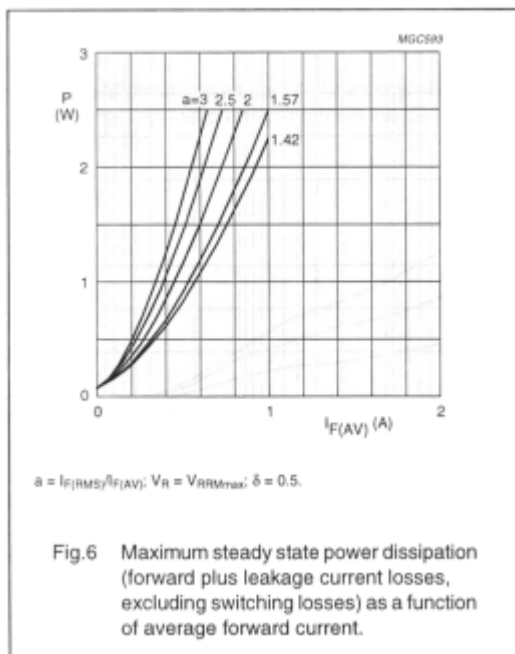
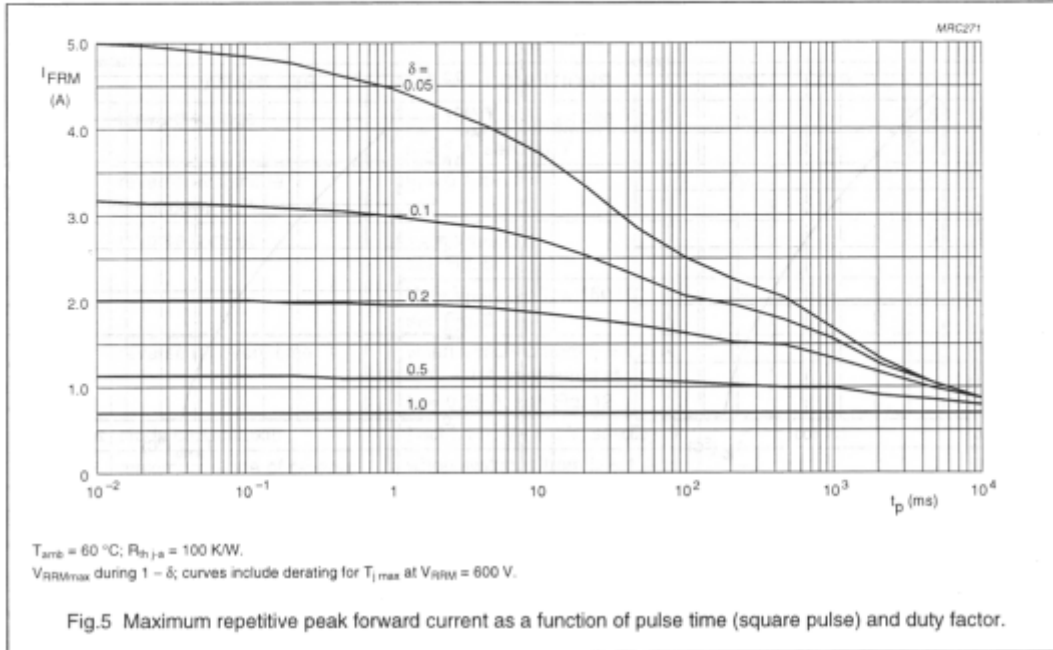
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GRAPHICAL DATA



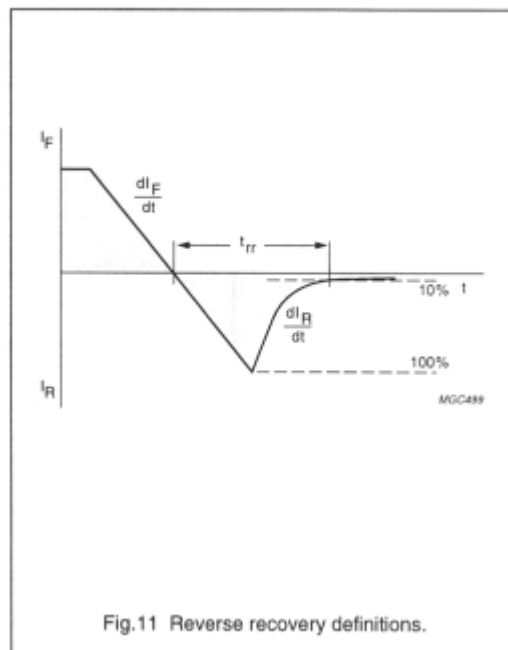
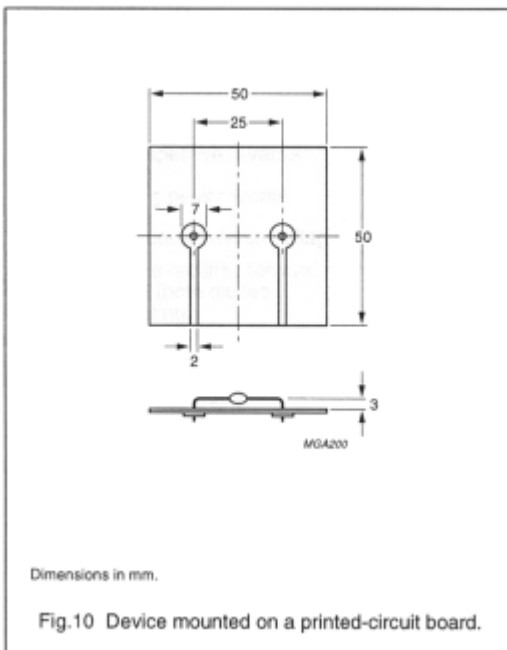
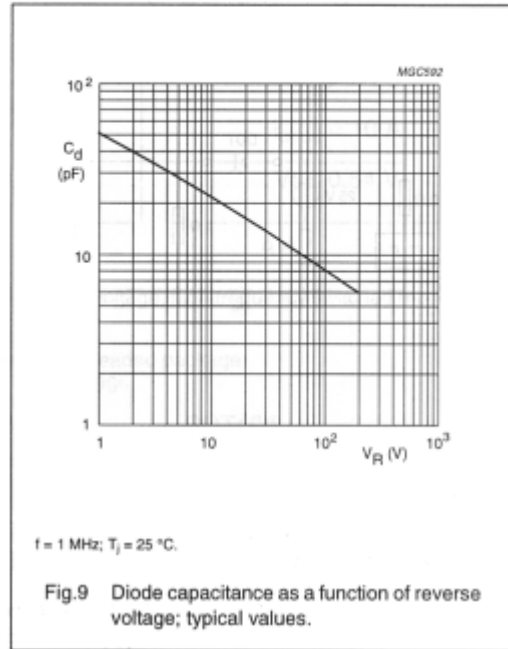
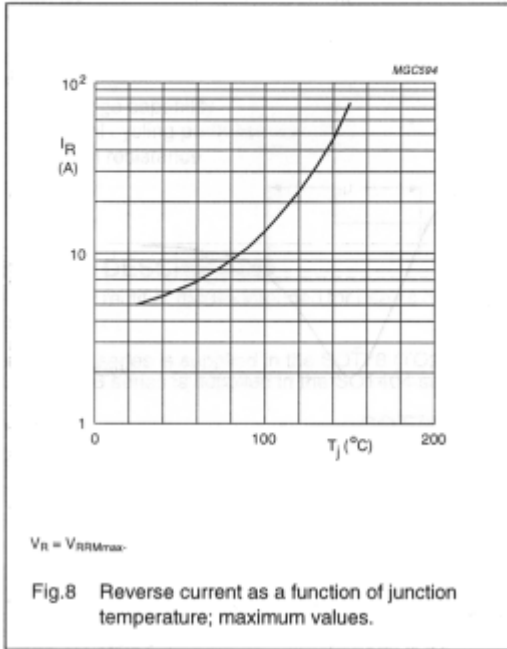
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